

The fetch decode execute cycle

Task 1

Use the Internet to research the fetch decode execute cycle. Describe how it works and where it is used.

Task 2

Describe at each stage of the process how the following registers are affected:

Program counter (PC)

Memory data register (MDR)

Memory address register (MAR)

Current instruction register (CIR)

Accumulator (ACC)

The buses

Arithmetic logic unit (ALU)

Control unit (CU)

Task 3

Use the animation features in PowerPoint or Flash to create an animation showing how two numbers would be loaded from memory and added together using the Fetch decode execute cycle.